

VP-2101

3U OPENVPXTM MULTI
PURPOSE IO MODULE

General Description

The VP-2101 is a 3U OpenVPXTM board for general purpose I/O applications requiring air-cooled or conduction cooled equipment. It is designed for the most demanding applications, combining system-on-chip capabilities together with the flexibility of interfaces customization via several available add-on modules.

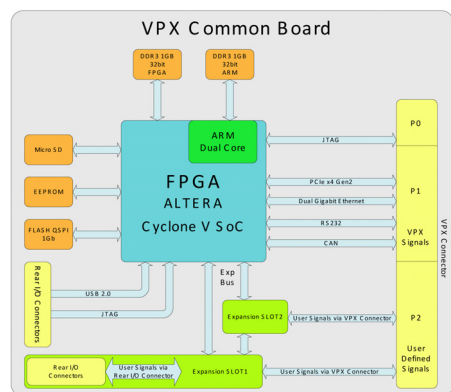
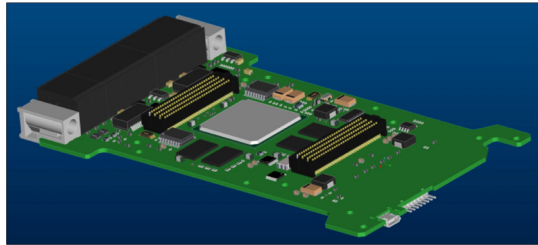
DO-254 certification package and manufacturing data package are also available on customer demand.



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Highlights

- 3U OpenVPXTM form factor
- Altera Cyclone VTM SoC FPGA
- Flexible add-on modules architecture
- SD card
- Dual 1Gbit Ethernet
- PCIe Gen2 x4 interface
- DDR3 on-board memories
- Serial Flash device
- I2C, USB 2.0 and JTAG interfaces

Expansion slots signals

- 3 x clk signals (25 MHz s.e. + 125 MHz differential + User defined s.e.)
- 2 x transceiver links up to 5Gbit/s to FPGA
- 16 TX/RX LVDS pairs + 2 LVDS clock signals to FPGA
- 22 single ended signals from Slot1/Slot2 to FPGA
- 72 user defined signals from Slot1/Slot2 to P2 connector
- 8 single ended signals from Slot1 to Slot2
- Power supplies (2.5V, 3.3V, 5V, 12V, 3.3V Aux, 12V Aux, -12V Aux)

Ordering Informations

- VP-2101/A 3U OpenVPXTM air-cooled board
- VP-2101/C 3U OpenVPXTM conduction-cooled board
- VP-2101/S Linux Device Driver
- VP-2101/R VxWorks Device Driver
- VP-2101/T Rear transition module

For more detailed information about any of our products, please do not hesitate to contact us.

Technical Specifications

• Compliance

- 3U OpenVPX (VITA65)/(VITA46)/(VITA48)
- MOD3-PAY-2F2T-16.2.5-3

• Power Consumption 16W (est. average)

• Memory

- Two independent 1GByte DDR3 SDRAM
- 64 MBytes SPI Flash
- 512 Kbytes serial EEPROM
- SD card interface

• FPGA Altera Cyclone VTM SoC Device (5CSTFD5D5F3117N)

• Inputs/Outputs

- PCIe Gen2 x4 interface
- Two 1Gbit Ethernet ports
- USB 2.0 interface
- Custom I/O via ad-on modules

• Board Services

- I2C sensors
- Power Management
- Reconfiguration on the filed
- Error logging

• Tools

- JTAG compliant
- Rear transition module
- Altera Signal TapTM debugging

